



4-7 December 2023 - Hilton Maslak İstanbul / Türkiye



ICECS 2023 PROGRAM AT A GLANCE

Sund	day, 3 December 2023	The Grand Ballroom 1 (Oral Sessions)	The Grand Ballroom 2 (Oral Sessions)	The Grand Ballroom 3 (Oral Sessions)	Azure (Oral Sessions)	Peridot (Oral Sessions)	Foyer (Poster Sessions)	
	8:00-17:30 9:00-10:30			Tutorial 1 - part 1	Tutorial 2 most 4		Registration Desk	
	10:30-11:00				Tutoriai 3 - part 1		<u>.</u> 1	
Day (11:00-12:30 12:30-14:00			Tutorial 1 - part 2	Tutorial 3 - part 2		<u>.</u>	
	14:00-15:30 15:30-16:00			Satellite WS - part 1	l	Tutorial 4 - part 1	Coffee Break	
-	16:00-17:30			Satellite WS - part 2 DAY 1		Tutorial 4 - part 2		
	8:00-20:30			DALL			Registration Desk	
'Monday, 4 December 2023	9:30-9:40	Opening Ceremony						
	9:40-10:40	Keynote 1 - Mehmet Toner						
		,						
	10:40-11:00			Coffee	Break			
	11:00–12:40	O 1: Modulators	O 2: Bio-Medical, Bio-Inspired CAS	O 3: SS2 - SS2: Hypercomplex NNs for CAS	O 4: Amplifiers	O 5: EDA: Test and Reliability	Demo & Industry	
	12:40-14:00			LUI	NCH			
	14:00–15:40	O 6: A/D Converters I	O 7: FPGA Applications II	O 8: SS1 - SS1: Neuromorphic Devices, Circuits and Systems I	O 9: Energy-Efficient Circuits & Systems	O 10: EDA: Tools, Design, Optimization	POSTER 1: Embedded Micro/Systems & Sensors + 2 ONLINE POSTER	
	15:40-16:00			Coffee	Break			
	16:00–17:40	O 11: A/D Converters II	O 12: Neural Networks II: Machine/Deep Learning	O 13: SS1: Neuromorphic Devices, Circuits and Systems II	O 14: Analog & Digital CAS Applications	O 15: Digital Circuits and Systems: Logic Cells	POSTER 2: EDA & Low-Power, Low- Voltage Systems	
	19:00-20:30	Welcome Cocktail DAY 2						
Tuesday, 5 December 2023	8:00-16:40						Registration Desk	
	9:30–10:40	Keynote 2 - Burak Göktürk						
	10:40-11:00			Coffee	Break			
	11:00–12:40	O 16: Analog Sub-Circuit Designs	O 17: SS5 - II: Amplifiers	O 18: SS1: Neuromorphic Devices, Circuits and Systems III	O 19: SS3: Defense Industry Technologies	O 20: Sensors and Sensing Systems I	POSTER 3: Bio-Medical, Audio, Image, and Video Processing CAS + 2 ONLINE POSTER	
	12:40-14:00			LUI	NCH			
	14:00–15:40	O 21: Low-Power, Low-Voltage Design	O 22: SS5 - I: Antennas & Filters	O 23: SS1: Neuromorphic Devices, Circuits and Systems IV	O 24: Circuits & Systems for Image Processing	O 25: Sensors and Sensing Systems II	POSTER 4: Machine/Deep Learning and Other Smart Systems for CAS Applications	
	15:40-16:00			Coffee	Break			
	16:00–17:40	O 26: Circuits & Systems for Communications	O 27: SS5 - III: RF Applications	O 28: Memory Cell Circuits & Systems	O 29: Signal Processing in Circuits & Systems	O 30: Oscillators		
_	8-00-40-00	T		DAY 3			Registration Desk	
Wednesday, 6 December 2023	8:00-10:00						Registration Desk	
	8:30-9:30	Keynote 3 - Hakkı Kaya Ocakaçan (presenting city tour)						
	9:30–17:00		Bus city tour incl. museum visiting (lunch will be served during the trip)					
	19:00–21:00	Gala Dinner & Awards Ceremony						
		DAY 4						
	9:00-16:00		1				Registration Desk	
ır 2023	9:30–10:40	Keynote 4 - Luca Benini						
nbe	10:40-11:00			Coffee	Break			
Thursday, 7 December 2023	11:00–12:40	O 31: FPGA Applications I	O 32: Smart Systems for CAS Applications	O 33: Embedded and Micro/Systems	O 34: Phase-Locked Loop Circuits		POSTER 5: Analog/Mixed- Signal/Microwave/Power Circuits	
	12:40-14:00			LUI	NCH			
	14:00–15:40	O 35: Linear and Non-Linear Circuits & Systems	O 36: VLSI Systems With Applications	O 37: SS4: Artificial Intelligence Methods for Modeling and Understanding the Human Brain				
L	15:40–16:00	Closing Ceremony						
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